

```

        org 0H

stepper1 equ P1
stepper2 equ P1

main:
    mov stepper1, #0CH
    acall delay
    mov stepper2, #0CH
    acall delay
    mov stepper1, #04H
    acall delay
    mov stepper1, #06H
    acall delay
    mov stepper2, #04H
    acall delay
    mov stepper1, #04H
    acall delay
    mov stepper2, #0CH
    acall delay
    mov stepper1, #0CH
    acall delay

    sjmp main

delay:
    mov r7, #4
wait2:
    mov r6, #0FFH
wait1:
    mov r5, #0FFH
wait:
    djnz r5, wait
    djnz r6, wait1
    djnz r7, wait2
    ret
end
end

```